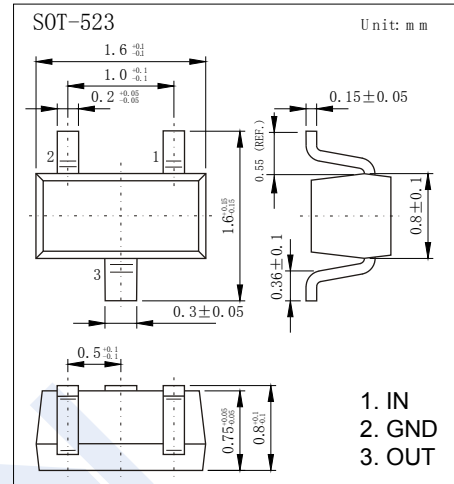
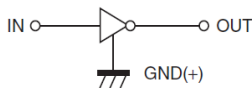
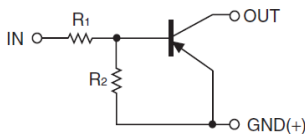


Digital Transistors

DTA114EE (KDTA114EE)

■ Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors(see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input.They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy



■ Absolute Maximum Ratings Ta = 25°C

Parameter	Symbol	Rating	Unit
Supply Voltage	V _{CC}	-50	V
Input Voltage	V _{IN}	-40~+10	
Output Current	I _O	-50	mA
Peak Collector Current	I _{CM}	-100	
Power Dissipation	P _D	150	mW
Junction Temperature	T _J	150	°C
Storage Temperature range	T _{stg}	-55 to 150	

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Input voltage	V _{I(off)}	V _{CC} = -5 V , I _O =-100uA	-0.5			V
	V _{I(on)}	V _O = -0.3 V , I _O =-10 mA			-3	
Output voltage	V _{O(on)}	I _O = -10 mA, I _I =-0.5 mA			-0.3	
Input current	I _I	V _I = -5 V			-0.88	mA
Output current	I _{O(off)}	V _{CC} = -50V , V _I =0			-0.5	uA
DC current gain	G _I	V _O =-5V,I _O =-5mA	30			
Input resistance	R ₁		7	10	13	KΩ
Resistance ratio	R ₂ /R ₁		0.8	1	1.2	
Transition frequency	f _T	V _O = -10V, I _O = -5mA,f=100MHz		250		MHz

■ Marking

Marking	14
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Digital Transistors

DTA114EE (KDTA114EE)

■ Typical Characteristics

